

2/23. # 2 tv.,

In re Application of: GURUPRASAD G. RAO ET AL.

Filed:

JULY 13, 2001

For:

APPARATUS AND METHODS FOR CONSTRAINT

CHARACTERIZATION WITH DEGRADATION OPTIONS

Serial No.:

09/904,630

Group Art Unit:

2123

Examiner:

UNKNOWN

Atty Dkt:

SIME:002

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OCT 23 2001

Technology Center 2100

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on the date below:

10-15-0

Name

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, it is respectfully requested that this Information Disclosure Statement be entered and the document(s) listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R §§ 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

The present Information Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be timely filed in accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with the filing of this Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Commissioner is hereby authorized to deduct said fees from Deposit Account No. 10-1205/SIME:002.

A copy of the listed document(s) required by 37 C.F.R. § 1.98(a)(2) is enclosed for the convenience of the Examiner.

Applicant respectfully requests that the listed document(s) be made of record in the case.

OCT 23 2001

Respectfully submitted, Technology Center 2100

Maximilian R. Peterson

Reg. No. 46,469

Attorney for Applicant

O'KEEFE, EGAN & PETERMAN, LLP 1101 Capital of Texas Highway South Building C, Suite 200 Austin, Texas 78746 (512) 347-1611 FAX: (512) 347-1615

Enclosures

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Form PTO-1449 (modified)	. 1001 1	Atty. Docket No.	Serial No.		
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List of Patents and Publications for	ASCRESSIVE	Applicant			
		GURUPRASAD RAO ET AL.			
INFORMATION DISCLOSURE S	TATEMENT				
		Filing Date:	Group:		
(Use several sheets if necessary)		JULY 13, 2001	2123		
U.S. Patent Documents	Foreign Pa	atent Documents	Other Art		
See Page	Se	ee Page	See Page		
U.S. Patent Documents					

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A1						-IVED
	A2					REC	
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	A6						
	A7						

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
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Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation				
	C1	J. Juan-Chico et al, "Delay Degradation Effect In Submicronic CMOS Inverters," presented at PATMOS, 1997, 5 pgs.				
	C2					
	C3					

Examiner:		-		Date Considered:
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EXAMINER: initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.